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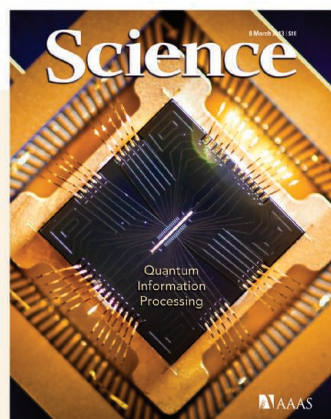
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COVER

Photograph of a silicon chip (area of entire chip: 33 mm²) that levitates individual atoms. Static and radio frequency voltages are applied to the array of electrodes at the center, creating an electric confinement for individual atomic ions, which hover ~0.1 mm above the chip. Such trapped ion systems are a leading physical implementation for quantum information processing. See the special section beginning on page 1163.

Photo: Curt Suplee and Emily Edwards, Joint Quantum Institute and University of Maryland; Chip fabrication: Sandia National Laboratories with support from the Intelligence Advanced Research Projects Activity

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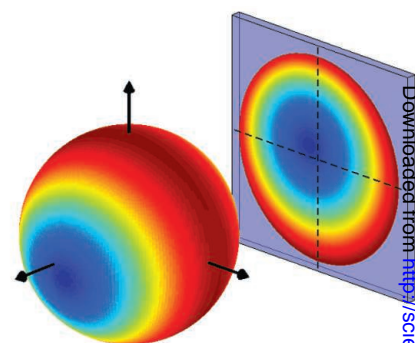
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SCIENCE (ISSN 0036-8075) is published weekly on Friday, except the last week in December, by the American Association for the Advancement of Science, 1200 New York Avenue, NW, Washington, DC 20005. Periodicals Mail postage (publication No. 484460) paid at Washington, DC, and additional mailing offices. Copyright © 2013 by the American Association for the Advancement of Science. The title SCIENCE is a registered trademark of the AAAS. Domestic individual membership and subscription (51 issues): \$149 (\$74 allocated to subscription). Domestic institutional subscription (51 issues): \$990; Foreign postage extra: Mexico, Caribbean (surface mail) \$55; other countries (air assist delivery) \$85. First class, airmail, student, and emeritus rates on request. Canadian rates with GST available upon request, GST #1254 88122. Publications Mail Agreement Number 1069624. Printed in the U.S.A.

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Science

339 (6124)

Science **339** (6124), 1121-1225.

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